PLANNED SYLLABUS COVERAGE (Theory)

	GP	Department: Co	mputer Engineering Subject : of Duration	perating	system	
F	Kangra	Course: Diplo	oma Durati	ion: 34e	ars	
SYL	LABUS /ERAGE	Total Period: 56 Theory: 56				
Sr No	Period Nos	Topic	Details	Instruction Reference	Additional Study Recommended	Remarks
1	1-9	Unit 1: Overview of Operating Systems	Objectives and Functions of Operating Systems, Operating Systems, Operating Systems Evolution - Batch Processing Systems, Multiprogramming Systems, Multiprocessing Systems, Time Sharing Systems, Personal Computer Operating Systems, Handheld Computer Systems, Real Time Systems, Distributed Systems; Operating System Architecture - Monolithic vs Microkernel		Modern Operating Systems by Andrew S. Tanenbaum	
2	10-20	Unit 2: Processes and Threads	Process, Process States, Process Life Cycle, Process Control Block (PCB), Threads, Multithreading, Inter-process Communication, Process Synchronization, Race Condition, Critical Section Problem and its Solutions, Deadlocks - Characterization, Necessary Conditions, Deadlock Avoidance, Prevention and Recovery.			
3	21-31	Unit 3 : CPU Scheduling	CPU Scheduler, Preemptive and Non-preemptive Scheduling, Scheduling Criteria - CPU Utilization, Throughput, Turnaround Time, Waiting Time, Response Time; Scheduling Algorithms - First Come First Serve, Shortest Job First,			

	7.48	Shortest Remaining Time First, Priority Scheduling, Round-Robin; Multiprocessor Scheduling	
4 32	Unit 4 : Mei Manageme		
5 41	Unit 5 : Stor Managemen		
6 49	Unit 6 : Linu Operating System	Features of Linux OS, GNU Project, Linux Architecture - Kernel, System Calls Interface, System Libraries, Shell	
App	roved	HOD Sign./PPL	

GP Kangra	Department: Computer Engineering Subject: Data Communication & Computer Networks	N-2022
	Course: Diploma Duration: 03 Years	Scheme
Syllabus	Total Periods: 16 Weeks / Sem	
Planned	Theory :48 Lectures $+ 16$ DCS $= 64$	
- rainica	Practical: - 01 Practical / Group	

SYLLABUS PLANNED

S. N.	Unit	Period No.	Topic Covered	Instruction Reference	Additional Study recommended	Remarks
01	01	1-14	Overview of Data Communication: Data Communication, Data Communication Characteristics - Delivery, Accuracy, Timeliness, Jitter; Components of Communication System, Data Flow - Simplex, Half-Duplex, Full-Duplex; Analog and Digital Signals, Peer-to-Peer and Client-Server Networks, Characteristics of Analog Signals - Frequency, Amplitude, Wavelength; Composite Signal, Phase, Bandwidth; Low Pass and Band Pass Channels, Baseband and Broadband Transmission, Data Rate Limit	Tanenbaum, PHI Porouzan, Tata McGraw Hill		
02	02	15-28	Computer Networks:- Objectives of Computer Networks, Applications, Network Protocols, Packet Switching, Circuit Switching, Network Topologies, Types of Computer Networks - PAN, LAN, MAN, WAN, Internetworks, Internet - History, Internet Infrastructure, DNS, Internet Routing Hierarchy	ork by Andrew S. Tane nd Networking by Forouz		
03	03	29-42	ISO- OSI Reference Model:- Advantages of Layered Network Architecture, ISO OSI Reference Model, Principles of OSI Reference Model, Functions of OSI Layers, Overview of Basic Protocols at Physical, Data Link, Network and Transport Layers	(i) Computer Network by Andrew S. Tanenbaum, PHI (ii) Data Communications and Networking by Forouzan, Tata McGraw Hill		

S. N.	Unit	Period No.	Topic Covered	Instruction Reference	Additional Study recommended	Remarks
04	04	43-54	Transmission Media and Networking Devices:- Wired Media - Coaxial, UTP, STP, Optical Fibre Cables; Wireless Media - Infrared, Radio Waves, Microwaves; Terrestrial and Satellite Wireless Communication; Transmission Impairments, Networking Devices - Repeater, Hub, Bridge, Switch, Router, Gateway, Modem	by Andrew S. Tata McGraw Hill		
05	05	55-64	TCP/IP Protocol Suite:- Layers in TCP/IP Protocol Suite, TCP/IP Protocol Data Units, IPv4 and IPv6 addresses, IPv4 CIDR Notation, Netmasks and Subnets, IPv4 Address Classes and Reserved Ranges, TCP and UDP, Ports, Well-known Ports, Telnet, FTP, SNMP, DHCP and DNS, Overview of Routing - Flooding, Distance Vector, Link State	9 5 0		

Date	10-08-2023
Approved	HOD Sign

PLANNED SYLLABUS COVERAGE (Theory)

GP	Department: (Computer Engineering Subject :CS	SA		
Kangra	Course: Dip	oloma Dura	tion: 3 years	S	1
SYLLABUS COVERAGE	Total Period:	Theory:		•	
Sr Period No Nos	Topic	Details	Instruction Reference	Additional Study Recommended	Remark
2.	Overview of Digital Electronics	Functional units of Digital Computer, Computer Organization, Computer Design, Computer Architecture, Von-Neumann and Harvard architecture, Bus Interconnection, Evolution of Microprocessors, Concept of Microcomputer, Microcontroller and Embedded Systems. Number systems:Decimal, Binary, Octal and Hexadecimal. Conversion from one number system to other number system. Signed binary Numbers: Sign Magnitude Representation, One's Compliment Representation and Two's Compliment Representation. Binary Arithmetic: Addition, Subtraction, Binary Arithmetic using one's and Two's Compliment. Fixed and Floating Point Numbers, Computer Codes: BCD, EBCDIC, ASCII. Multiplication Algorithms – Hardware Implementation for Signed-Magnitude Data, Booth Multiplication Algorithm.		Recommended	
	Digital Logic	Logic Gates: Symbols and Truth Table, Boolean Algebra, Logic Diagram, De-Morgan's Theorem, Combinational Circuits: Block Diagram, Half Adder, Full Adder, Flip Flop: SR, D Flip Flop and JK Flip Flop, Example of a sequential circuit, Decoder and Encoder: 3 to 8,			

		Demultiplexer: 1 to 4 line.		
	Basic Architecture of Microprocessor 8085	Basic features of 8085 Microprocessor, Block Diagran of 8085 Microprocessor, Functions of various blocks, Concept of Buses, Bus Multiplexing and De- multiplexing, status Flags, Addressing Modes and Interrupts.		
5.	Central Processing Unit	Major Components of CPU, General Register Organization, Control Word, Stack Organization- Register and Memory. Reverse Polish Notation and Evaluation of Arithmetic Expressions, Instruction formats – Three Address Instructions, Two Address Instructions, One Address Instructions, Zero Address Instructions Brief Introduction to RISC and CISC Pricessors, Concept of Parallel Processing and Pipelining.		
6.	Memory Organization	Components of memory hierarchy: Main memory, auxiliary memory and cache memory, Introduction to Associative Memory, Cache Memory- Locality of Reference, Hit ratio, Writing into Cache – Write Through, Write Back, Input-Output Interface – Purpose, I/O Versus Memory Bus, Isolated versus Memory-Mapped I/O.		

Approved	0 0 0	HOD Sign	
Date:	07/08/22	Chin	

PLANNED SYLLABUS COVERAGE (Theory)

	GP		nputer Engineering Subject: W	7 (Web	<i>lechnology</i>	es)
ŀ	Kangra	Course: Diplo	ma Durat	ion: 3 Ye	ars	
SYL	LABUS /ERAGE	Total Period: 42	Theor		Technology aus 3/week)	
Sr No	Period Nos	Topic	Details	Instruction Reference	Additional Study Recommended	Remark
1	1-7	Unit 1 : Internet and World Wide Web	Brief History of the Internet, Structure of the Internet, Internet Services and Applications, Different Ways to Connect to the Internet, Common Internet Connection Issues and their Solutions, World Wide Web, HTTP, Familiarization with the Key Terms - Network Protocol, Web Server, Web Browser, Website, Web Application, Hypertext, Hyperlink, Search Engine, Proxy Server, URL, DNS		HTML & CSS: Design and Build Websites, John Ducket, Wiley Publishing	
2	8-14	Unit 2 : HTML 5	HTML, HTML Coding Conventions, HTML Tag, Structure of HTML Element, Global Attributes - id, class, style, title, tabindex; Structure of a Web Page - <html>, <head>, <body>, <!DOCTYPE > , <title> and <meta> Elements; HTML Comments, Document Object Model (DOM)</td><td></td><td></td><td></td></tr><tr><td>3</td><td>15-23</td><td>Unit 3 : HTML
Basic Elements</td><td>Headings - <h1> <h6>; Paragraphs - , Special Text Elements - <pre></td><td></td><td></td><td></td></tr></tbody></table></title></body></head></html>			

			Lists: Ordered Llsts (), Unordered Lists (); Attributes of List Elements: type, start; Nested Lists, Line Break (> and Horizontal Rule (<hr/>>); Text Formatting Elements - , , , <i>, <mark>, <u>, _{, , <tfoot>, , , <thead>, ,</thead></tfoot>}</u></mark></i>	
4	24-28	Unit 4 : HTML Layout Elements	Block and Inline Elements, Creating Sections - <div>, ; Identifying Elements - id, class and name attributes; Frames - <iframe>; HTML5 Semantic Elements - <main>, <header>, <footer>, <article>, <section>, <nav>, <aside>, <details>, <summary>, <time>, <figure></figure></time></summary></details></aside></nav></section></article></footer></header></main></iframe></div>	
5	2935	Unit 5 : Cascading Style Sheets	CSS Types - Inline, Internal, External; <style> and <link> elements; CSS Rule, Selector and Declaration; CSS Length Units; CSS Box Model; Setting Margins, Borders and Padding of Elements; CSS Colors - Color Names, RGB and HEX Formats; Setting Colors of Text, Background and Border; Styling Text - font-family, font-size, font-style, font-weight, font-transform, fontdecoration,</td><td></td></tr></tbody></table></style>	

			text-align; CSS Layo and float; Flexbox ar layouts; Styling Table and Lists; Basic Anir CSS, CSS Pseudo E Pseudo Classes	d Grid es nation using	
6	36-42	Unit 6 : Javascript	Role of Javascript in Embedding Javascript Web Page, Javascript Naming, Scope and Hoisting; Javascript Control Statements; Arrays; Linking External Java Accessing and Manig HTML DOM Element Javascript; JavaScript BuiltinJavascript Fundefined Functions	ot Code in of Variables Lifetime, Operators, Javascript ascript File; oulating is with out.	
	Approved			D Sign.	